sponding to said multipath, respectively; an adder for adding together the likelihoods from said plurality of branch metric generating parts; and a maximum likelihood sequence estimator for selecting a symbol sequence candidate of a maximum likelihood function on the basis of the output from said adder and for outputting the result of decision of the selected symbol sequence candidate as the detected digital signal.

20. The receiver of claim **1**, wherein said multipath separating part and said diversity type detection part are each provided in a predetermined number larger than 2 and said spread baseband received signal from said receiving part is fed to each of said plurality of multipath separating parts, and further comprising a multiplexer for selectively coupling the detected digital signals from said predetermined diversity type detection part in a repeating cyclic order for each chip into a sequence of detected digital signals and a select signal generator for supplying said multiplexer with a select signal for designating that one of the diversity type detection parts which is to be selected by said multiplexer in correspondence with the bit rate of said transmitted signal, and wherein said short codes used in said predetermined number of multipath separating parts have the same chip number but differ from each other.

21. The receiver of claim **1**, wherein said receiving part and said multipath separating part are each provided in a predetermined number larger than 2 in correspondence with a predetermined number of antennas larger than 2, and wherein said diversity type detection part diversity-detects despread signals corresponding to respective multipath components, respectively fed thereto from said predetermined number of multipath separating parts and outputs the detected digital signal.

22. The receiver of claim **20** or **21**, wherein each of said multipath separating parts includes a delay circuit for setting a delay time corresponding to the delay time of a path delayed relative to said direct path between respective timings for despreading said spread baseband received signal by said pair of short and long codes in said predetermined number of despreading parts.

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